L Number	Hits	Search Text	DB	Time stamp
1	56279	INPUT adj port	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 16:54
2	71783	output adj port	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 16:54
3	31724	(INPUT adj port) same (output adj port)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 16:54
4	161	((INPUT adj port) same (output adj port)) same ((semiconductor adj memory) or sram or sdram)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:08
5	301931	unidirectional or uni-directional or (one adj direction)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 16:56
6	3415	(unidirectional or uni-directional or (one adj direction)) with bus	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 16:56
7	7	(((INPUT adj port) same (output adj port)) same ((semiconductor adj memory) or sram or sdram)) and ((unidirectional or uni-directional or (one adj direction)) with bus)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:07
8	6044	plurality with (INPUT adj port)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:07
9	4060	(plurality with (INPUT adj port)) same (output adj port)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:07
10	109098	((semiconductor adj memory) or sram or sdram)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:08
11	250	((plurality with (INPUT adj port)) same (output adj port)) and (((semiconductor adj memory) or sram or sdram))	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:08
12	7	(((plurality with (INPUT adj port)) same (output adj port)) and (((semiconductor adj memory) or sram or sdram))) and ((unidirectional or uni-directional or (one adj direction)) with bus)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:08
13	5	((((plurality with (INPUT adj port)) same (output adj port)) and (((semiconductor adj memory) or sram or sdram))) and ((unidirectional or uni-directional or (one adj direction)) with bus)) not ((((INPUT adj port) same (output adj port)) same ((semiconductor adj memory) or sram or sdram)) and ((unidirectional or uni-directional or (one adj direction)) with bus))	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:14
14	569	((plurality with (INPUT adj port)) same (output adj port)) same memory	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:14
15	12	(((plurality with (INPUT adj port)) same (output adj port)) same memory) and ((unidirectional or uni-directional or (one adj direction)) with bus)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:15

16	9	((((plurality with (INPUT adj port)) same (output adj port)) same memory) and ((unidirectional or uni-directional or (one adj direction)) with bus)) not (((((plurality with (INPUT adj port)) same (output adj port)) and (((semiconductor adj memory) or sram or sdram))) and ((unidirectional or uni-directional or (one adj direction)) with bus)) not ((((INPUT adj port) same (output adj port)) same ((semiconductor adj memory) or sram or sdram)) and ((unidirectional or uni-directional or (one adj direction)) with bus)))	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:15
17	7	(((((plurality with (INPUT adj port)) same (output adj port)) same memory) and ((unidirectional or uni-directional or (one adj direction)) with bus)) not (((((plurality with (INPUT adj port)) same (output adj port)) and (((semiconductor adj memory) or sram or sdram))) and ((unidirectional or uni-directional or (one adj direction)) with bus)) not ((((INPUT adj port) same (output adj port)) same ((semiconductor adj memory) or sram or sdram)) and ((unidirectional or uni-directional or (one adj direction)) with bus)))) not ((((INPUT adj port) same (output adj port)) same ((semiconductor adj memory) or sram or sdram)) and ((unidirectional or uni-directional or (one adj direction)) with bus))	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:32
18	3056	standard adj mode	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:32
19	24250	address adj register	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:33
20	28884	data adj register	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:33
21	91	(standard adj mode) and (address adj register) and (data adj register)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:33
22	28463	16-bit or 16bit	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:34
23	77	((standard adj mode) and (address adj register) and (data adj register)) and (16-bit or 16bit)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:34
24	22854	32-bit or 32bit	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:34
25	72	(((standard adj mode) and (address adj register) and (data adj register)) and (16-bit or 16bit)) and (32-bit or 32bit)	USPAT; US-PGPUB; EPO; DERWENT	2004/05/26 17:35